

<p>FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>	Attorney Docket Number	42113/PAN/B600
	Application Number	10/004,246
	Filing Date	November 2, 2001
	Applicant(s)	John R. Nickolls, et al.
	Group Art Unit	To be Assigned
	Examiner Name	To be Assigned

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - kind code ² (If known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
<i>[Signature]</i>	1	5,745,721	04-28-1998	Beard et al.
<i>[Signature]</i>	2	5,832,290	11-03-1998	Gostin et al.

FOREIGN PATENT DOCUMENTS					
EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ . Kind Code ⁵ (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)
<i>[Signature]</i>	3	EP 0-227 900 A2	07-08-1987	Wolfgang et al.	✓

OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER SIGNATURE	<i>[Signature]</i>	DATE CONSIDERED	<i>10/22/04</i>
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FORM PTO-1449A/B (10-01)
Schedule for PTO-1449A/B

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Attorney Docket Number

42113/GDL/B600

Application Number

US01/45810

Filing Date

November 2, 2001

Applicant(s)

John R. Nickolls, et al.

Group Art Unit

2183

Examiner Name

Dale A. Hall

U.S. PATENT DOCUMENTS



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FOREIGN PATENT DOCUMENTS

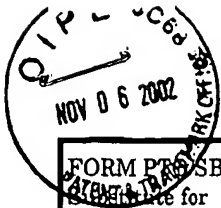
EXAMINER INITIALS	Cite No. ¹	<u>Foreign Patent Document</u> Country Code ³ - Number ⁴ - Kind Code ⁵ (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)
12		EP 0 729 091 A	08/28/1996	France Telecom	
25		WO 00 49496 A	08/24/2000	Koninklijke Philips Electronics N.V.	

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		JACOB, ET AL., "Memory Interfacing Instruction Specification for Reconfigurable Processors," ACM/SIGDA International Symposium on Field Programmable Gate Arrays. FPGA '99. Monterey, CA, February 21-23, 1999, New York, NY, 21 February 1999 (1999-02-21), pages 145-154, XP000868486, ISBN: 1-58113-088-0, page 147-page 148
		WO 99 42922 A (KNUTH ROBERT); Siemens AG (DE); SHENDEROVITCH GEORGIY (IL); GRANOT) 26August 1999 (1999-08-26)

EXAMINER SIGNATURE	<i>Henry Tran</i>	DATE CONSIDERED	10/22/04
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FORM PTO/SB/08A/B (10-01) Information Disclosure Statement for PTO-1449A/B INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Attorney Docket Number	42113/GDL/B600
	Application Number	US01/45810
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145		"Selecting Predecoded Instructions with a Surrogate," IBM Technical Disclosure Bulletin, IBM Corp., New York; US, vol. 36, no. 6A, 1 June 1993 (1993-06-01) pages 35-38, XP000370750, ISSN: 0018-8689
126		Search Report, Patent Cooperation Treaty, 09/23/2002, 6 pages

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